

## IEEE VLSI PROJECTS LISTS

<b>PROJECT CODE</b>	<b>PROJECT TITLE</b>
VTVL01	AMCAL: Approximate Multiplier with the Configurable Accuracy Levels for Image Processing and Convolutional Neural Network
VTVL02	Approximate MAC unit using Static Segmentation
VTVL03	Exact Versus Inexact Decimal Floating-Point Numbers and Arithmetic
VTVL04	HUB meets Posit: Arithmetic Units Implementation
VTVL05	Novel Low-Power Floating-Point Divider with Linear Approximation and Minimum Mean Relative Error
VTVL06	Numerical Model for 32-Bit Magnonic Ripple Carry Adder